



Form 1449 (Modified)

Information Disclosure
Statement By Applicant

(Use Several Sheets if Necessary)

Atty Docket No.

PETAP003

Applicant:

Shah et al.

Filing Date

April 13, 2001

Application No.:

09/834,964

Group

2667

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
CPG	A	6,646,983 B1	11/11/03	Roy et al.			11/21/00
	B						
	C						
	D						
	E						
	F						
	G						
	H						
	I						

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	
	J							
	K							
	L							
	M							
	N							

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	O	
	P	
	Q	
Examiner	Christopher G.	Date Considered 7/10/05

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
CPG	A	2002/0012340	01/2002	Kalkunte et al.			
CPG	B	2002/0012341	01/2002	Battle et al.			
CPG	C	2002/0013621	09/2002	Battle et al.			
CPG	D	2002/0027908	03/2002	Kalkunte et al.			
CPG	E	2003/0097467	05/2003	Sano			
CPG	F	2003/0118016	06/2003	Kalkunte et al.			
CPG	G	2003/0198231	10/2003	Kalkunte et al.			
CPG	H	4,965,788	10/1990	Newman			
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CPG	L	5,703,879	12/1997	Proctor et al.			
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CPG	O	6,067,286	05/2000	Jones et al.			
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CPG	T	6,438,106	08/2002	Pillar et al.			
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CPG	V	6,535,510	03/2003	Kalkunte et al.			
CPG	W	6,542,507	04/2003	Khacherian et al.			

Examiner	Christopher Grey	Date Considered	6/10/05
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U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-class	Filing Date
CPG	X	6,567,417	05/2003	Kalkunte et al.			
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CPG	B1	6,658,016	12/2003	Dai et al.			
CPG	C1	6,680,910	01/2004	Ni, Jie			
CPG	D1	6,721,273	04/2004	Lyon, Norman A.			

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
CPG	E1	“Common Switch Interface Specification-L1, version 1.0”, CSIX, August 5, 2000, http://www.csix.org .
CPG	F1	M. Shreedhar and George Varghese, “Efficient Fair Queuing Using Deficit Round-Robin”, IEEE/ACM Transactions on Networking, Vol. 4, No. 3, June 1996.
CPG	G1	Nick McKeown, “iSLIP: The iSLIP Scheduling Algorithm for Input-Queued Switches”, IEEE/ACM Transactions on Networking, Vol. 7, No. 2, pp. 188-201, April 1999.
CPG	H1	Kenneth Y. Yun, “A Terabit Multi-Service Switch With Quality Of Service Support”, HOT Interconnect 8, pp. 21-30, August 2000.
CPG	I1	Nick McKeown, “Fast Switched Backplane for a Gigabit Switched Router”, Department of Engineering, Stanford University, Stanford, CA
CPG	J1	Prabhakar et al., “Multicast Scheduling for Input-Queued Switches”, IEEE Journal on Selected Areas in Communications, May 1996.
CPG	K1	Ohsaki et al., “Performance of an Input/Output Buffered-Type ATM LAN Switch with Back-Pressure Function”, IEEE/ACM Transactions on Networking, Vol. 5, No. 2, April 1997.
CPG	L1	McKeown et al., “The Tiny Tera: A Packet Switch Core”, Hot Interconnects V, Stanford University, August 1996.
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